


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No.
Applicant(s): <b>Brian W. Curran et al.</b>			<b>POU920000107US1</b>
Application No. <b>09/841,505</b>	Filing Date <b>April 24, 2001</b>	Examiner <b>Magid Y. Dimyan</b>	Group Art Unit <b>2825</b>
Invention: <b>CMOS Tapered Gate and Synthesis Method</b>			
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I hereby certify that this <u>                    <b>Response to Notice of Non-Compliance</b> dated 3/16/05                    </u> <small>(Identify type of correspondence)</small>			
is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>          <b>703-872-9306</b>          </u> )			
on <u>          <b>March 28, 2005</b>          </u> <small>(Date)</small>			
 <b>Susan L. Nelson</b> <small>(Typed or Printed Name of Person Signing Certificate)</small>  <small>(Signature)</small>			
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IBM Docket No. PO920000107US1 Ser. No.:  
09/841,505 Examiner M. Dimyan

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International Business Machines Corporation

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March 28, 2005 By Facsimile to to (703) 872-9306

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
TRANSMITTAL LETTER, FEE AUTHORIZATION  
and RESPONSE TO OFFICE ACTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

R: Patent Appln. USSN: 09/841,505 Filed: April 24, 2001 Action Day: Oct 29, 2004  
Title: CMOS tapered gate and synthesis method  
Inventor(s): Brian W. Curran et al.  
Examiner: Magid Y Dimyan Group Art: 2825  
Attorney Docket No.: PO9-2000-0107US1  
Attorney/Agent: Lynn L. Augspurger, Reg. No.: 24,227 Deposit Acct: 09-0463

Dear Sir:

RESTATED CLAIMS ARE SUBMITTED HERewith IN RESPONSE TO  
NOTICE OF NON-COMPLIANT AMENDMENT NOTICE Mailed 3/16/2005

Transmitted herewith for filing is a 1.131 RESPONSE TO OFFICIAL ACTION  
EXPEDITED PROCEDURE REQUESTED with remarks.

☒ Please charge my Deposit Account No. 09-0463 in the amount  
of \$ 00.00. (No changes in claim numbers.)

This charge authorization is for all properly calculated fees.

A duplicate copy of this sheet is enclosed.

☒ The Commissioner is also hereby authorized to charge payment of the  
following fees associated with this communication or credit any  
overpayment to Deposit Account No. 09-0463.

☒ Any additional filing fees required under 37 CFR Sec. 1.16

☒ Any patent application processing fees under 37 CFR Sec. 1.17

☒ Any Fee for extension of time for response (None expected.).

If there are any questions regarding this application, please call the undersigned attorney.

Respectfully,

Lynn L. Augspurger, Attorney Reg. No. 24,227

IBM Docket No. PO920000107US1 Ser. No.:  
09/841,505 Examiner M. Dimyan

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In re: Patent Appln. USSN: 09/841,505 Filed: April 24, 2001 Action Day: Oct 29, 2004  
Title: CMOS tapered gate and synthesis method  
Inventor(s): Brian W. Curran et al.  
Examiner: Magid Y Dimyan Group Art: 5674  
Attorney Docket No.: PO9-2000-0107US1  
Attorney/Agent: Lynn L. Augspurger, Reg. No.: 24,227 Deposit Acct: 09-0463

Dear Sir:

This is the Applicants response to the Notice of Non-Compliant Amendment of mailed 3/16/2005 resubmitting the claims with this facsimile. The undersigned talked March 28, 2005 with Legal Instruments Examiner Amanda Ford and learned that the claims 6,7,8,9 should be renumbered (as they are now presented) as claims 5,6,7,8 because a fifth claim was not present on filing. Double brackets are used also. Examiner Ford is thanked for her courteous assistance.

Please enter this 1.131 RESPONSE TO OFFICIAL ACTION, affidavits and remarks.

Please Amend the claims to read as follows: